2-day Hands-on National Workshop on "FPGA Design Flow using Vivado" Concludes Successfully at Department of Electronic Engineering, MUET, Jamshoro

The Department of Electronic Engineering, Mehran University of Engineering and Technology, Jamshoro, successfully organized a Two-Day Hands-on National Workshop on "FPGA Design Flow using Vivado" on 25th and 26th September 2024. The workshop was led by Prof. Dr. Farida Memon, Professor, Department of Electronic Engineering, as the Resource Person, while the event's organizing and supporting team included Engr. Shoaib Hassan Khaskheli, Senior Instrumentation Engineer, along with Asfandyar Jamali, Shayan Ahmed Khan, and Maheen Nawaz. Engr. Kamran Kazi, Assistant Professor and Convener of the Departmental Event Management Committee (EMC), also played a key role as an organizer.



The Inauguration Ceremony was held on 25th September 2024, with **Prof. Dr. Aftab Ahmed Memon**, Dean of the Faculty of Electrical, Electronics, and Computer Engineering (FEECE), as the **Chief Guest**. He shared his insightful thoughts on the significance of FPGA technology in modern engineering practices and emphasized the importance of hands-on workshops for students' professional development. The ceremony was also graced by **Prof. Dr. Wajiha Shah**, Chairperson of the Department of Electronic Engineering, along with other esteemed faculty members.



The Concluding Ceremony took place on 26th September 2024, where Prof. Dr. Wajiha Shah served as the Chief Guest. During her address, she expressed her gratitude to Prof. Dr. Farida Memon, the resource person for the workshop, and acknowledged the efforts of the organizing teams, including the volunteers and the Event Management Committee (EMC). Certificates were distributed to the resource person, participants, and volunteers as a token of appreciation. Additionally, a shield was presented to Prof. Dr. Farida Memon as a mark of recognition for her contributions to the workshop.





Engr. Kamran Kazi, Convener of the EMC, extended his appreciation to **Prof. Dr. Wajiha** Shah for her continuous support throughout the event. He also thanked Engr. Shoaib Hassan Khaskheli, the lead organizer, along with the dedicated volunteers, for their tireless efforts in ensuring the success of the workshop.



The two-day event provided participants with practical experience and in-depth knowledge of FPGA design using the Vivado tool. This workshop has equipped them with essential skills that will be invaluable in their academic and professional pursuits.